

SYSTEM AND METHOD FOR UTILIZING A MEMORY DEVICE
TO SUPPORT ISOCHRONOUS PROCESSES

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ABSTRACT OF THE DISCLOSURE

A system and method for utilizing a memory device to support isochronous processes comprises a memory device that may be partitioned to provide an isochronous memory for storing high-priority isochronous information, and a processor device for accessing and utilizing the isochronous information that is stored in the isochronous memory. The isochronous memory is reserved for storing the isochronous information, and may be reconfigured into a selectable number of memory channels of varying size that each corresponds to an associated isochronous process.

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